

## Syllabus of B. Tech. Computer Engineering (COE) for 4<sup>th</sup> Semester

Course Title	Probability Theory	Course No	To be filled by the office		
Specialization	Mathematics	Structure (IPC)	3	0	3
Offered for	B.Tech. (COE, EDM), DD (CED, ESD, EVD)	Status	Core <input checked="" type="checkbox"/>	Elective <input type="checkbox"/>	
Course Objectives	To impart knowledge of basic concepts and applications of Probability and Statistics				
Course Outcomes	At the end of the course, a student will be able to apply the knowledge in solving engineering problems				
Contents of the course (With approximate break up of hours)	<p>Introduction to Probability: Sets, Events, Axioms of Probability, Conditional Probability and Independence, Bayes Theorem and MAP Decision Rule (8)</p> <p>Random Variables: Definitions, Cumulative Distribution Functions, mass and density functions, joint and conditional distributions, Functions of Random Variables (8)</p> <p>Expectations: Mean, Variance, Moments, Correlation, Chebychev and Schwarz Inequalities, Moment-generating and Characteristic Functions, Chernoff Bounds, Conditional Expectations (8)</p> <p>Random Vectors: Jointly Gaussian random variables, Covariance Matrices, Linear Transformations, Diagonalization of Covariance Matrices (6)</p> <p>Random Sequences: Sequences of independent random variables, correlation functions, wide-sense stationary sequences, LTI filtering of sequences (6)</p> <p>Law of Large Numbers, Central Limit Theorem (6)</p>				
Textbook	<ol style="list-style-type: none"> <li>1. Stark and Woods, "Probability and Random Processes with Applications to Signal Processing," 3<sup>rd</sup> Edition, Pearson Education 2002.</li> <li>2. S. Ross, "A First Course in Probability," 6<sup>th</sup> Edition, Pearson.</li> </ol>				
References	<ol style="list-style-type: none"> <li>1. J. S. Milton and J. Arnold, Introduction to Probability and Statistics, Tata McGraw Hill Education Private Limited, 4<sup>th</sup> Edition, 2006.</li> <li>2. S. Kay, Intuitive Probability and Random Processes Using MATLAB, Springer, 2008.</li> <li>3. R. M. Gray and L. D. Davison, "An Introduction to Statistical Signal Processing," Cambridge University Press, 2004.</li> </ol>				

Course Title	Sociology of Design	Course No	To be filled by the office		
Specialization	Management	Structure (LTFC)	3	0	3
Offered for	UG and DD	Status	Core <input checked="" type="checkbox"/>	Elective <input type="checkbox"/>	
Pre-requisite	None	To take effect from			
Course Objectives	Design as a Social Activity – Level 1				
Course Outcomes	<p>This course will help students understand</p> <ul style="list-style-type: none"> <li>• Design as a social activity involving people, their relationships &amp; values - How designs can emerge out of or be constrained by social patterns of relating</li> <li>• How technology can influence interactions among people, cooperative work, ethical issues around technology interventions</li> <li>• Exposure to techniques like ethnomethodology</li> </ul>				
Contents of the course (With approximate break up of hours)	<p>Basics concepts of sociology (behavior, interaction, language) [6]</p> <p>Historical evolution of Societies (Agrarian, Industrial, Digital) and current human and organizational contexts in which engineers and other professionals work, Personal and corporate social responsibility &amp; ethics [10]</p> <p>Relationship between people (age, gender, cultures) and technology - Social and psychological dimensions of technological change, Technology &amp; Work, Co-operative Work &amp; Coordinative Practices, Ethnomethodology, Critical Systems Heuristics [10]</p>				
Textbook and References	<ol style="list-style-type: none"> <li>1. Manuel Castells (1996); The Rise of Network Society.</li> <li>2. Herbert Blumer (1986); Symbolic Interactionism: Perspective and Method.</li> <li>3. Herkert, J. (ed.), Social, Ethical, and Policy Implications of Engineering: Selected Readings. New York, NY: IEEE Press, 2000.</li> <li>4. Heath, C. and Luff, P. (2000); Technology in Action, Cambridge: Cambridge Univ Press.</li> <li>5. Werner Ulrich (1983), Critical Systems Heuristics, John Wiley, London.</li> </ol>				

Course Title	Design and Analysis of Algorithms	Course No	To be filled by the office		
Specialization	Computer Engineering	Structure (IPC)	3	0	3
Offered for	UG and DD	Status	Core <input checked="" type="checkbox"/>	Elective <input type="checkbox"/>	
Course Objectives	Data Structure and Algorithm course is essential to understand many areas in Computer Science and Engineering. This course also trains the students to solve problems using computer.				
Course Outcomes	At the end of the course, students will be able to design data structures and efficient algorithms to solve given problem.				
Contents of the course	<p>Introduction to Asymptotic Notation – Solving Recurrence relations – Master’s theorem – Recurrence Tree method (8)</p> <p>Incremental and Decremental Algorithm Design Strategies – case studies, lower bound for sorting (3)</p> <p>Divide &amp; Conquer – Merge – Quick sort – Median Finding- (6)</p> <p>Greedy algorithms – knapsack problem (fractional and 0/1 versions) - Minimum spanning tree – Prims- Kruskal’s algorithm- Huffman coding, Set of Intervals (6)</p> <p>Dynamic programming – case studies — LCS-Matrix Multiplication – Knapsack (7)</p> <p>Graph algorithms – Topological sort – Shortest path algorithms – Dijkstra’s Algorithm, – Bellman-Ford’s Algorithm (5)</p> <p>Solvability &amp; Tractability – Introduction to unsolvable problem-Hatling problem- Introduction to NP-completeness – Search/Decision, SAT, Independent set, VC, X3C, Hamilton circuit, etc Backtracking – n queen problem-subset problem - Branch &amp; Bound- Job Scheduling problem (10)</p>				
Textbook	1. E. Horowitz, S. Sahni, and S. Rajasekaran, “Computer Algorithms,” 2 <sup>nd</sup> Edition, Galgotia Publications, 2007.				
References	<p>1. T. H. Cormen, C. E. Leiserson, and R. L. Rivest, “Introduction to Algorithms,” Prentice Hall India, 2<sup>nd</sup> Edition, 2001.</p> <p>2. Aho, Hopcroft, and Ullmann, “Data Structures &amp; Algorithms,” Addison Wesley, 1983.</p>				

Course Title	Database Systems	Course No	To be filled by the office		
Specialization	Computer Engineering	Structure (IPC)	3	0	3
Offered for	UG and DD	Status	Core <input checked="" type="checkbox"/>	Elective <input type="checkbox"/>	
Course Objectives	The focus of this course is on database design, architecture, and relational models. Normal forms, internal schema design would also be explored				
Course Outcomes	Learner would appreciate the systematic design and principles involved in any database development. The importance of canonical normal forms and its design in large scale database systems would be a secondary outcome of this course				
Contents of the course	<p>Introduction to Database Systems, Database System Architecture, Schema, Database Models, Relational Model, ER Modelling and case studies. (7)</p> <p>Expressive power of relational databases, Relational Algebra (5)</p> <p>Database Languages, DDL, DML, Structured Query Language (SQL), SQL views, case studies (8)</p> <p>Database Design, Normal Forms (First to third normal form), Boyce codd Normal Form, Database decomposition, Functional Dependencies, Loss-less Join decomposition(8)</p> <p>Transaction Processing and Concurrency control (4)</p> <p>Internal schema Design, Indexing, B-trees, B+ trees (5)</p> <p>Introduction to advanced concepts like Data mining, Data warehousing, XML (5)</p>				
Textbook	<ol style="list-style-type: none"> <li>1. R. Elmasri and S. B. Navathe, "Fundamentals of Database Systems," Pearson, 4<sup>th</sup> Edition, 2007.</li> </ol>				
References	<ol style="list-style-type: none"> <li>1. A. Silberschatz, H. F. Korth, and S. Sudharsan, "Database System Concepts," Tata McGraw Hill, 5<sup>th</sup> Edition, 2006.</li> <li>2. C. J. Date, A. Kannan, and S. Swamynathan, "An Introduction to Database Systems," Pearson, 8<sup>th</sup> Edition, 2006.</li> </ol>				

Course Title	Computer Organization and Design	Course No	To be filled by the office		
Specialization	Computer Engineering	Structure (IPC)	3	0	3
Offered for	UG and DD	Status	Core <input checked="" type="checkbox"/>	Elective <input type="checkbox"/>	
Course Objectives	The course aims to introduce various aspects of computer organization such as Instruction format, Instruction codes, Addressing Modes, processor design and hierarchical memory design, Input and Output Interface design using Programmed Controlled and Interrupt Control way				
Course Outcomes	Students will be able to interface and program various components such as Memory, I/O, etc. with the processor.				
Contents of the course	<p>Introduction: function and structure of a computer, functional components of a computer, performance of a computer system. Instruction set architectures – CISC and RISC architectures. (5)</p> <p>Instructions: Language of the Computer, Operations of the Computer Hardware, Operands of the Computer Hardware, Representing Instructions in the Computer, Logical Operations Instructions for Making Decisions, addressing Modes, Parallelism &amp; Instructions. (5)</p> <p>Arithmetic Design: – Carry look ahead adder, Wallace tree multiplier, Floating–point adder/subtractor, Division. (5)</p> <p>The Processor: Logic Design Conventions, Building a Datapath, A Simple Implementation Scheme (3)</p> <p>An Overview of Pipelining, Pipelined Data path and Control, Data Hazards: Forwarding versus Stalling, Control Hazards, Exceptions and Parallelism via Instructions. (7)</p> <p>Memory Hierarchy: Introduction, Memory Technologies (SRAM, DRAM), The Basics of Caches, Measuring and Improving Cache Performance, Dependable Memory, Virtual Machines, Virtual Memory, A Common Framework for Memory Hierarchy, Using a Finite-State Machine to Control a Simple Cache, Parallelism and Memory Hierarchies: Cache Coherence, Parallelism and Memory Hierarchy: Redundant Arrays of Inexpensive Disks and Implementing Cache Controllers. (9)</p> <p>Input/Output Unit: access of I/O devices, I/O ports, I/O control mechanisms – Program Controlled I/O. Interrupt controlled I/O and DMA controlled I/O; I/O interfaces – Serial port, parallel port, USB port, SCSI bus, PCI bus; I/O peripherals – Keyboard, display, secondary storage devices. (8)</p>				
Textbook	<ol style="list-style-type: none"> <li>1. Patterson and Hennessy, “Computer Organization and Design,” Morgan Kaufmann, 5<sup>th</sup> Edition, 2013.</li> <li>2. C. Hamacher, Z. Vranesic, and S. Zaky, “Computer Organization,” Tata McGraw Hill, 5<sup>th</sup> Edition, 2002.</li> </ol>				
References	<ol style="list-style-type: none"> <li>1. J. P. Hayes, “Computer Architecture and Organization,” Tata McGraw Hill 1998.</li> <li>2. M. J. Murdocca, V. P. Heuring, “Computer Architecture and Organization - An Integrated Approach,” John Wiley &amp; Sons Inc., 2007.</li> <li>3. A. S. Tanenbaum, “Structured Computer Organization,” Prentice Hall, 5<sup>th</sup> Edition, 2006.</li> </ol>				

Course Title	Object Oriented Algorithm Design and Analysis Practice	Course No	To be filled by the office		
Specialization	Computer Engineering	Structure (IPC)	0	3	2
Offered for	UG and DD	Status	Core <input checked="" type="checkbox"/>	Elective <input type="checkbox"/>	
Course Objectives	The objective is to introduce object oriented programming (OOP) paradigm and implement algorithms using OOP concepts.				
Course Outcomes	Students would be capable of using OOP concepts effectively while implementing various algorithmic paradigms.				
Contents of the course	<p>The laboratory component will require the student to write computer programs using a careful choice of data structures and algorithmic paradigms (in C++/Java language) from scratch, based on the concepts learnt in the theory course.</p> <p><b>OOP concepts:</b> Object oriented programming - Encapsulation – Constructors – Destructors - Composition – Friend functions/classes – this pointer – Dynamic memory management  Operator overloading Reusability – Inheritance – Base &amp; derived classes – Protected members – Constructors –Destructors in derived classes – public/private/protected inheritance–Polymorphism  Virtual functions - Templates – Function &amp; Class templates – Streams – Stream input  Output Stream format states – Manipulators – Exception handling – Re-throwing exceptions – specifications–and exception handling – Inheritance – STL</p> <p>Case studies involving Data structures and Algorithms using OOPs concepts.</p>				
Textbook	1. P. J. Deitel and H. M. Deitel, “C++ : How To Program,” Prentice Hall, 8 <sup>th</sup> Edition, 2011.				
References	1. H. Schildt, “Teach Yourself C++,” 3 <sup>rd</sup> Edition, Tata McGraw Hill. 2. R. Lafore, “Object Oriented Programming in C++,” 4 <sup>th</sup> Edition, Sams Publishing.				

Course Title	Database Systems Practice	Course No	To be filled by the office		
Specialization	Computer Engineering	Structure (IPC)	0	3	2
Offered for	UG and DD	Status	Core <input checked="" type="checkbox"/>	Elective <input type="checkbox"/>	
Course Objectives	This course introduces SQL programming. Database design preserving functional dependencies and loss-less decomposition properties would be addressed.				
Course Outcomes	Conceptual design using ER diagrams, programming using structured query language, and database design respecting third normal form shall be the outcomes of this course.				
Contents of the course	Introduction to SQL. Schema, table creation using SQL, Data definition and data manipulation using SQL. Implementation of set theoretic operations on databases. Views using SQL. Implementation of algorithms related to functional dependencies and loss-less decomposition. Indexing using B-trees and B+ trees( creation, insertion, deletion).				
Textbook	<ol style="list-style-type: none"> <li>1. Loney Koch, Oracle – The complete reference, Tata McGraw Hill, 2002</li> <li>2. R.Elmasri and S.B.Navathe, Fundamentals of Database Systems, Pearson, 4<sup>th</sup>Edn, 2007.</li> </ol>				
References	<ol style="list-style-type: none"> <li>1. A. Silberschatz, H. F. Korth, and S. Sudharsan, “Database System Concepts,” Tata McGraw Hill, 5<sup>th</sup> Edition, 2006.</li> <li>2. C. J. Date, A. Kannan, and S. Swamynathan, “An Introduction to Database Systems,” Pearson, 8<sup>th</sup> Edition, 2006.</li> </ol>				

Course Title	Computer Organization & Design Practice	Course No	To be filled by the office		
Specialization	Computer Engineering	Structure (IPC)	0	3	2
Offered for	UG and DD	Status	Core <input checked="" type="checkbox"/>	Elective <input type="checkbox"/>	
Course Objectives	Exposure to assembly language programming, instruction set design, and processor design for a given instruction set are given. Assembler macros, interrupt service routines, and simple device driver programs would also be introduced. Computer system design concepts are introduced.				
Course Outcomes	Students would be able to demonstrate programming proficiency using the various addressing modes and data transfer instructions of the target computer, and design microcomputer systems.				
Contents of the course	Exercises will mainly involve writing the assembly language programs - Execution of assembly language programs: Single-step, break points, Accessing the contents of registers, accessing the contents of memory locations - Implementation of higher level language assignment statements with arithmetic expressions and logical expressions - Implementation of control transfer statements. Macros - Software interrupts - Operating system function calls - Interrupt service routines - Simple device drivers - Assembly language programming in C language. I/O interfacing and programming. Computer System Design.				
Textbook	1. Patterson and Hennessy, "Computer Organization and Design," Morgan Kaufmann, 5 <sup>th</sup> Edition, 2013.				
References	1. C. Hamacher, Z. Vranesic, and S. Zaky, "Computer Organizaton," Tata McGraw Hill, 2002.				